



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:)	Group Art Unit: Unknown
)	
Zampardi, et al.)	Examiner: Unknown
)	
Serial No.: 10/692,134)	
)	
Filed: 10/23/2003)	
)	
For: BiFET Including a FET Having)	
Increased Linearity and)	
Manufacturability)	
_____)	

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir or Madam:

In accordance with 37 C.F.R. 1.56 and 1.97, Applicant hereby discloses to the Patent Office patents, publications or other information of which Applicant is aware. A copy of a Form 1449 identifying the other materials and copies of two technical papers are submitted herewith.

The items identified in this Information Disclosure Statement may or may not be "material" as defined in 37 C.F.R. 1.56 and the submission thereof by Applicant is not to be construed as an admission that any such patent, publication or other information referred to is material or considered to be material (37 C.F.R. 1.97(h)), or even qualifies as "prior art" under 35 U.S.C. 102 with respect to the present invention unless specifically designated by Applicant as such. Identification of any reference or patent herein is not an admission, nor is it to be construed as an admission, that it was invented prior to the invention disclosed herein.

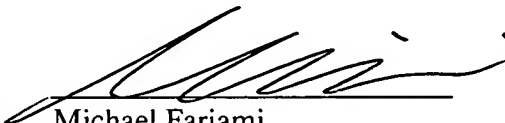
The filing of this Information Disclosure Statement is not to be construed to mean that a search has been made or that no other material information, as defined in 37 C.F.R. 1.56, exists.

In accordance with 37 C.F.R. §1.97(b)(3) no fee is enclosed, since the Information Disclosure Statement is being filed before the mailing date of a first office action on the merits.

The Assistant Commissioner is hereby authorized to charge or credit Deposit Account Number 50-0731 for any deficiency or overpayment in the fees required for the filing of this Disclosure Statement, for which purpose a duplicate copy of this paper is also included.

Respectfully Submitted;
Farjami & Farjami LLP

Dated: 9/15/04

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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FORM PTO-1449

ATTY. DOCKET NO.
0140113SERIAL NO.
10/692,134LIST OF PATENTS AND OTHER ITEMS
FOR APPLICANT'S INFORMATION
DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANTS:
Zampardi, et al.FILING DATE:
October 23, 2003GROUP ART:
Not Assigned

U.S. PATENT DOCUMENTS

Exam. Initials		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
	AA						
	AB						
	AC						

FOREIGN PATENT DOCUMENT

Exam. Initials		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	BA							
	BB							
	BC							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	CA	Fresina et al.: "Selective Self-Aligned Emitter Ledge Formation for Heterojunction Bipolar Transistors," IEEE Electron Device Letters, Vol. 17, No. 12, December, 1996, pages 555-556
	CB	Fresina et al.: "InGaP/GaAs HBT with Novel Layer Structure for Emitter Ledge Fabrication," IEEE, 0-7803-3393-4, 1996, pages 207-210

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.